



Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
IS9-017/SERIAL NO.
09/904.578LIST OF ARTICLES BY APPLICANT
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Richard C. Eden et al.FILING DATE
July 12, 2001GROUP
2812

U.S. PATENT DOCUMENTS

*Examiner/ Initial/		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

DF	AO		"Active Clamp Circuits for Switchmode Regulators Supplying Microprocessor loads" Sanders et al.; 5/1997; pps. 1179-1185.					
DF	AP		"An Active Clamp Circuit for Voltage Regulation Module (VRM) Applications"; Wu et al.; IEEE Transactions on Power Electronics, Vol. 16, No. 5; 9/2001; pps. 623-634					
DF	AQ		"An Integrated Controller for a High Frequency Buck Converter"; Lau et al.; 5/1997; pps. 246-254.					
DF	AR		"A Low-Voltage CMOS DC-DC Converter for a Portable Battery-Operated System"; Stratakos et al; 5/1994; pps. 619-626.					
DF	AS		"International Rectifier Introduces HexFET® Power MOS in FlipFET™ Packages Enabling World's Highest Power Density"; http://www.irf.com/whats-new/nr000612a.html ; June 2000; 2 pps.					
DF	AT		U.S. Patent Application Pub. No. US 2001/0038277 A1; Publication Date 11/8/2001; Burnstein et al.					
DF	AU		U.S. Patent Application Pub. No. US 2001/0035746 A1; Publication Date 11/1/2001; Burnstein et al.					

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EVO 85429393

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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
IS9-018/SERIAL NO.
09/905, 187LIST OF ART CITATIONS
(Use several sheets if necessary)APPLICANT
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2814

U.S. PATENT DOCUMENTS

*Examiner/ Initial/		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
DF	AA	6,329,801 B1	12/11/01	Zuniga et al.			
DF	AB	6,278,264 B1	8/21/01	Burnstein et al.			
DF	AC	6,271,651 B1	8/7/01	Stratakos et al.			
DF	AD	6,268,716 B1	7/31/01	Burnstein et al.			
DF	AE	6,225,795 B1	5/1/01	Stratakos et al.			
DF	AF	6,198,261 B1	3/6/01	Schultz et al.			
DF	AG	6,160,441	12/12/00	Stratakos et al.			
DF	AH	6,100,676	8/8/00	Burnstein et al.			
DF	AI	6,031,361	2/29/00	Burnstein et al.			
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DF	AK	5,969,513	10/19/99	Clark			
DF	AL	5,744,843	4/28/98	Efland et al.			
DF	AM	6,150,150	11/21/00	Livi et al.			
DF	AN	6,351,108 B1	2/26/02	Burnstein et al.			

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AO							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

DF	AR		"Synchronous Rectification with Adaptive Timing Control"; Acker et al.; 06/95; pps. 88-95.					
DF	AS		"High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications"; Stratakos; The 12th International Symposium on Power Semiconductor Devices & ICs; 1998; pps. 1-245.					
DF	AT		"A Low Voltage CMOS Compatible Power MOSFET for On-Chip Dynamic Voltage Scaling DC-DC Converters" Nassif-Khalil et al.; 1/2000; pps. 43-46					
DF	AU		"FlipFET™ MOSFET Design for High Volume SMT Assembly"; Schofield et al.; http://www.irf.com/technical-info/papers.html ; 3/18/2001; 6 pps.					

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